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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/747,599	12/22/2000	Glynn Russell Ashdown	P98,1245	4501
7590 12/17/2003			EXAMINER	
Mr. W. Daniel Swayze, Jr.			NGUYEN, DILINH P	
Texas Instrum	ents Incorporated			
P.O. Box 655474			ART UNIT	PAPER NUMBER
M/S 3999			2814	
Dallas, TX 75265			DATE MAIL UD. 12/17/200	2

Please find below and/or attached an Office communication concerning this application or proceeding.

CONTRACTOR OF THE PARTY.

Application/Control Number: 09/747,599

Art Unit: 2814

DETAILED ACTION

Claim Rejections - 35 USC § 103

- The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior at are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- Claims 1-7, 13-14 and 17-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Rife et al. (U.S. Pat. 5945736) in view of Villani (U.S. Pat. 5329426).
 - Regarding claims 1, 13-14 and 17-18, Rife et al. disclose a semiconductor device (fig. 1, column 5, lines 26 et seq.) comprising:

a circuit board 112 having a mounting pad provided with an adhesive material in a mounting region (column 1, lines 15-18);

a mounting plate 116 formed of a thermally conductive material, the mounting plate having a first major surface being positioned on the mounting pad of the circuit board:

a heat dissipation element 146/120 thermally connected to the mounting plate and being spaced form the circuit board, the heat dissipating element being disposed in a position to receive air flow on both sides:

a heat generating component 118 mounted on the mounting plate at a second major surface opposite the first major surface, the heat dissipating element being spaced from the heat generating component to permit air flow between the heat dissipating element and the heat generating component; and

Application/Control Number: 09/747,599
Art Unit: 2814

an extension 126 generally perpendicular to the mounting plate in a direction opposite the first major surface and extending substantially along the entire length of the mounting plate 116.

Rife et al. fail to disclose the mounting plate defining a plurality of adhesive flow openings therethrough.

Villani discloses a semiconductor device (fig. 1, column 2, lines 25 et seq.) comprising: a mounting plate 12 defining a plurality of openings 16 therethrough.

Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the device of Rife et al. to provide a good thermal contact and to form a pin grid array for the semiconductor package device, as shown by Villani

- Regarding claim 2, Rife et al. disclose the adhesive material is electrical solder (column 1, lines 15-18).
- Regarding claim 3, Rife et al. disclose the adhesive material is thermal adhesive.
- Regarding claims 4-7, Rife et al. disclose the heat dissipation element 146/120 includes: a portion generally parallel to the mounting plate and spaced therefrom; wherein the portion overlies the mounting plate; wherein the portion includes lateral extensions and wherein the mounting plate the extension and the portion form a U shape.
- Regarding claims 19-20, Rife et al. disclose the circuit board is in a first plane,
 the heat dissipating element is in a second plane and the heat generating
 element is in the third plane, the first and second planes are space apart and the

Application/Control Number: 09/747,599 Page 4

Art Unit: 2814

third plane having the heat generating element is disposed between the first and second plane and wherein the first, second and third planes are substantially parallel to one another.

Allowable Subject Matter

Claim 8 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

REASONS FOR ALLOWANCE

Claims 9 and 15-16 are allowed.

The following is an examiner's statement of reasons for allowance:

Kikushima, Rife et al. and Villani fail to disclose the combination of all the limitations recited, including a channel along an edge of the mounting plate, wherein the channel receiving a tab extending from the heat generating component.

Therefore, the overall structure of the heat sink assembly is neither anticipated nor rendered obvious over the prior art of record.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance"

Art Unit: 2814

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DiLinh Nguyen whose telephone number is (571) 272-1712. The examiner can normally be reached on 8:00AM - 6:00PM (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 308-7724 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-

DLN December 11, 2003